

IC Design at Fraunhofer FEP: From technologies to applications

You want a perfect job *from student to PhD* in Europe's largest R&D organization

- with *flexible working hours*
- to contribute to *major societal challenges*, e.g. by designing highly energy-efficient IC
- *responsibility* and to work on your *own public and industrial projects*
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Explore with us OLED-based microdisplays and sensor components

- Custom- and application-specific device and *integrated circuitry (IC) design*
 - Analog, digital, mixed-signal, in foundry processes
- *Microdisplays & Sensors* by silicon and organic microelectronics
 - OLED-on-silicon technology
 - Augmented-/virtual-reality eyewear (AR/VR/MR)
- *CMOS-integrated optoelectronics*



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Wearables for Industry & Consumer



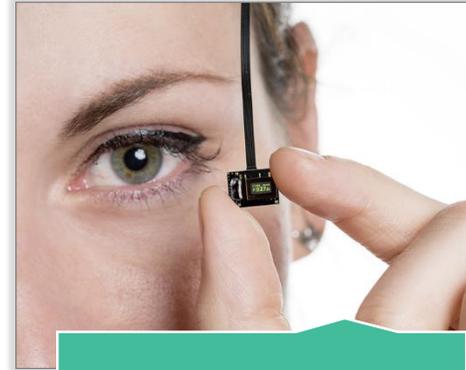
Augmented Reality



Virtual Reality



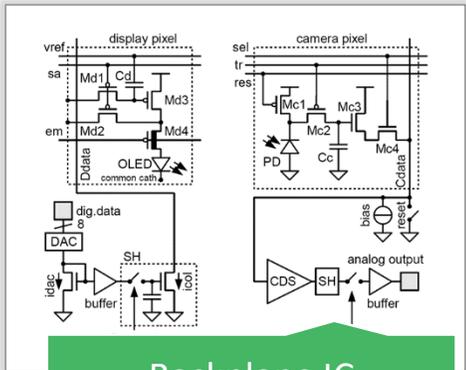
Assisted Reality



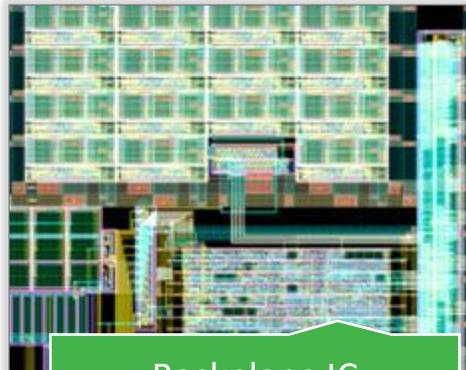
Microdisplays



Embedded Design



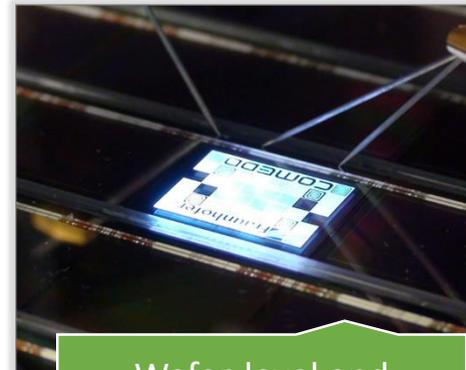
Backplane IC Schematic Design



Backplane IC Layout Design



Frontplane Integration



Wafer-level and Device Testing

Bildquellen

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